Q.P. Code: 18MC9123

Reg. No:

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS)

MCA II Year II Semester Supplementary Examinations July-2021 **SOFTWARE TESTING**

Time: 3 hours Max. Marks: 60

(Answer all Five Units $5 \times 12 = 60$ Marks)

UNIT-I

a Explain the concepts i) Path Predicates ii) Path Sensitizing **8M b** Explain the taxonomy of Bugs? 4M

OR

State and explain various dichotomies in software testing. **6M b** Explain goals for testing and model for testing in software testing. 6M

UNIT-II

Define transaction flow graph and define transaction with an example. **6M**

b Explain various loops with an example. **6M**

OR

What are data-flow anomalies? How data flow testing can explore them? 12M

UNIT-III

Discuss with example the equal - span range/Doman compatibility bugs. 12M 5

OR

a Explain about linear domain boundaries. **6M**

b Explain about complete domain boundaries. **6M**

UNIT-IV

Write Short Notes on Distributive Laws 4M

Discuss path sums, path products and absorption laws. **8M**

OR

Explain Regular Expressions and Flow Anomaly detection. 8 12M

UNIT-V

Write testers comments about state graphs 6M

b Write the design guidelines for building finite state machine into code. 6M

OR

a What are the types of bugs that can cause state graphs? 10 6M

b Explain about good state and bad state graphs. 6M

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